



PATENT
Attorney Docket No. 401558

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Takao HASEGAWA

Application No. 10/073,328

Filed: February 13, 2002

Art Unit: 2825

Examiner: L. Garbowski

For: METHOD OF WIRING
SEMICONDUCTOR INTEGRATED
CIRCUIT, SEMICONDUCTOR
INTEGRATED CIRCUIT, AND
COMPUTER PRODUCT

RESPONSE TO OFFICE ACTION

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action dated September 30, 2003, please enter the following amendments and consider the following remarks.

